

<b>Notice of References Cited</b>	Application/Control No. 10/075,149	Applicant(s)/Patent Under Reexamination ALEKSIC ET AL.	
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U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
*	A	US-6,175,889	01-2001	Olarig, Sompong Paul	710/309
*	B	US-6,175,888	01-2001	Guthrie et al.	710/310
*	C	US-6,480,917	11-2002	Moertl et al.	710/119
	D	US-			
	E	US-			
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	J	US-			
	K	US-			
	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Definition of North Bridge from Wikipedia
	V	Definition of South Bridge from Wikipedia
	W	Definition of RDRAM from Wikipedia
	X	Intel 810 Chipset Review, X-Bit Labs, 1999

\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)  
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.

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**NON-PATENT DOCUMENTS**

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	How Motherboards Work from Howstuffworks.com
	V	
	W	
	X	

\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)  
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.